



# ISE 6 – The Low Cost, High Performance Logic Design Solution

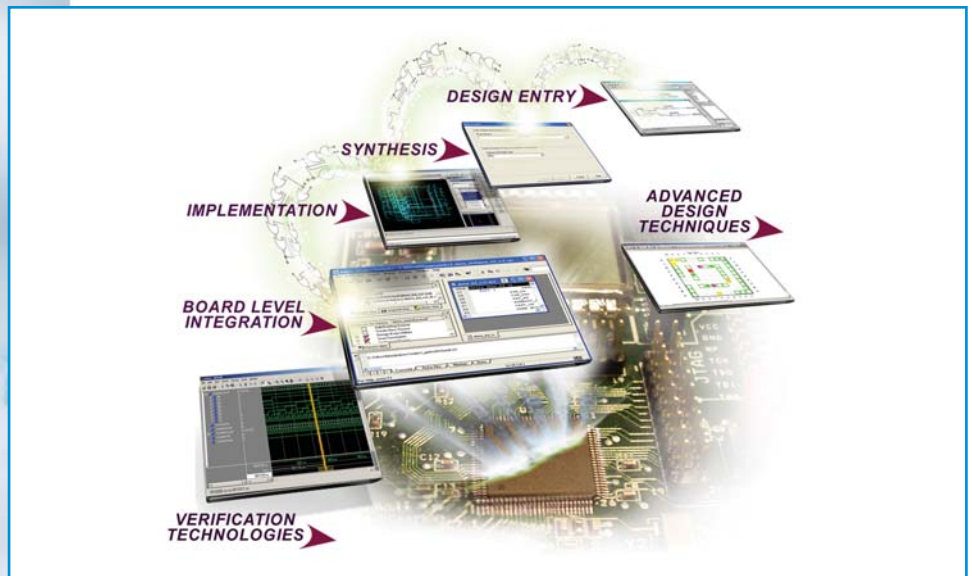
The Design Tool Chosen #1 by FPGA Designers

ISE 6, the Integrated Software Environment from Xilinx, provides the fastest, most complete, integrated family of design tools available anywhere—earning Xilinx the #1 rating in an independent survey of FPGA designers.

When used with the new Virtex-4™ FPGA family, ISE 6 lets designers achieve the fastest performance in the market today with up to 200,000 logic cells and up to 500 MHz performance. Together, Virtex-4 and ISE 6 deliver twice the density and faster performance than any other FPGA offering.

Advanced technology such as PACE for pin mapping and logic area definition, Timing-Driven Mapping, and Project Navigator let you take maximum advantage of leading Xilinx devices like Spartan-3™ FPGAs — which offer the lowest cost per-gate and lowest cost per-I/O available.

Xilinx backs this robust, easy-to-use design environment with the industry's best standard hotline support and one of the world's Top 10 Web Support Sites, [mysupport.xilinx.com](http://mysupport.xilinx.com). All the tools you need to complete your designs faster, at lower cost, and with the highest logic performance.



## Powerful Design Tools from the Industry Leader

In an independent 2004 industry FPGA survey, design engineers chose Xilinx as #1 in all 22 surveyed categories, including technology leadership, best integration with other vendors' tools, support, documentation, and more. Learn more about what FPGA engineers are saying at [www.xilinx.com/leader](http://www.xilinx.com/leader)

## Highest Push-button Logic Performance

ISE 6 delivers the fastest performance available in programmable logic design — up to 500 MHz when using the new leading-edge Virtex-4 platform FPGAs — and more high-speed design capabilities than any competing FPGA offering. Plus, ISE provides access to XtremeDSP™ 11.1 Gbps RocketIO™ multi-gigabit transceivers, Smart RAM, Digital Clock Managers, and much more, to offer a truly unique design platform.

## Lowest Project Cost

ISE helps you lower project costs with high-performance configurations at the lowest design tool prices. Advanced ISE algorithms like timing-driven mapping can deliver up to 30% better design performance through more efficient design packing and placement, helping you stay in lower density, lower cost devices like Spartan-3 FPGAs.

## Unparalleled Ease-of-Use

ISE 6 provides the most user-friendly integrated software environment for logic design, including Architecture Wizards that help you configure advanced hardware features through a quick and easy-to-use graphical interface. ISE 6 also enables you to improve your overall PCB layout with tools like PACE, which simplifies the complicated pin-management and area-constraints definition process.

For more information on how ISE can boost your design performance and lower your design cost, visit [www.xilinx.com/ise](http://www.xilinx.com/ise)



	Feature	ISE WebPACK™	ISE BaseX	ISE Foundation	ISE Alliance
Devices	Virtex™ Series Including the new Virtex-4 Family	Virtex-E™: V50E -V300E Virtex-II™: 2V40 - 2V250 Virtex-II Pro™: 2VP2	Virtex: V50 - V600 Virtex-E: V50E - V600E Virtex-II: 2V40 - 2V500 Virtex-II Pro: 2VP2, 2VP4, 2VP7	ALL	ALL
	Spartan™ Series	SpartanII/III: ALL (except XC2S400E and XC2S600E) Spartan-3™: 3S50, 3S200, 3S400	Spartan-II/III: ALL Spartan-3: 3S50, 3S200, 3S400	Spartan-II/III: ALL Spartan-3: ALL	Spartan-II/III: ALL Spartan-3: ALL
	CoolRunner™ XPLA3 CoolRunner-II CoolRunner-IIA	ALL	ALL	ALL	ALL
	XC9500 Series	ALL	ALL	ALL	ALL
Design Entry	Schematic Editor	Yes	MS Windows and Linux only	MS Windows and Linux only	No
	HDL Editor	Yes	Yes	Yes	Yes
	State Diagram Editor	Yes	MS Windows only	MS Windows only	MS Windows only
	CORE Generator System	No	Yes	Yes	Yes
	PACE (Pinout and Area Constraint Editor)	Yes	Yes	Yes	Yes
	Architecture Wizards Clocking Wizard RocketIO™ Wizard ChipSync™ Wizard (Virtex-4 Only) XtremeDSP™ Slice Wizard (Virtex-4 Only)	Yes	Yes	Yes	Yes
	Third Party RTL Checker Support	Yes	Yes	Yes	Yes
	Xilinx System Generator for DSP	No	Sold as an Option	Sold as an Option	Sold as an Option
Embedded System Design	Embedded Design Kit (EDK)	No	Sold as an Option	Sold as an Option	Sold as an Option
	Xilinx Synthesis Technology (XST)	Yes	Yes	Yes	No
	Synplicity Synplify/Pro	Integrated Interface	Integrated Interface	Integrated Interface	Integrated Interface
	Synplicity Amplify Physical Synthesis Support	Integrated Interface	Integrated Interface	Integrated Interface	Integrated Interface
	Synopsys FPGA Compiler II	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
	Synopsys DC-FPGA Compiler	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
	Mentor Graphics LeonardoSpectrum	Integrated Interface	Integrated Interface	Integrated Interface	Integrated Interface
	Mentor Graphics Precision® RTL	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
	Mentor Graphics Precision® Physical	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
ABEL	CPLD	CPLD (MS Windows only)	CPLD (MS Windows only)	CPLD (MS Windows only)	
Implementation	FloorPlanner	Yes	Yes	Yes	Yes
	PlanAhead™ Hierarchical Floorplanner	Sold as an Option	Sold as an Option	Sold as an Option	Sold as an Option
	Constraints Editor	Yes	Yes	Yes	Yes
	Timing Driven Place and Route	Yes	Yes	Yes	Yes
	Modular Design	No	Yes	Yes	Yes
	Incremental Design	Yes	Yes	Yes	Yes
	Timing Improvement Wizard	Yes	Yes	Yes	Yes
Programming	iMPACT	Yes	Yes	Yes	Yes
	System ACE™ Configuration Manager	Yes	Yes	Yes	Yes
Board Level Integration	IBIS Models	Yes	Yes	Yes	Yes
	STAMP Models	Yes	Yes	Yes	Yes
	HSPICE Models*	Yes	Yes	Yes	Yes
Verification	ChipScope™ Pro	Sold as an Option	Sold as an Option	Sold as an Option	Sold as an Option
	HDL Benchmer™	Yes	MS Windows only	MS Windows only	MS Windows only
	ModelSim® Xilinx Edition (MXE II)	ModelSim XE II Starter**	ModelSim XE II Starter**	ModelSim XE II Starter**	ModelSim XE II Starter**
	Static Timing Analyzer	Yes	Yes	Yes	Yes
	FPGA Editor with Probe	No	Yes	Yes	Yes
	ChipViewer	Yes	Yes	Yes	Yes
	XPower (Power Analysis)	Yes	Yes	Yes	Yes
	Third Party Equivalency Checking Support	Yes	Yes	Yes	Yes
	SMARTModels for PowerPC™ and RocketIO™	No	Yes	Yes	Yes
Third Party Simulator Support	Yes	Yes	Yes	Yes	
Platforms	PC (MS Windows 2000/MS Windows XP)	PC (MS Windows 2000/MS Windows XP), Linux	PC (MS Windows 2000/MS Windows XP), Sun Solaris, Linux	PC (MS Windows 2000/MS Windows XP), Sun Solaris, Linux	
IP/CORE	For more information on the complete list of Xilinx IP products, visit the Xilinx IP Center at <a href="http://www.xilinx.com/ipcenter">http://www.xilinx.com/ipcenter</a>				

\*HSPICE Models available at the Xilinx Design Tools Center at [www.xilinx.com/ise](http://www.xilinx.com/ise), \*\*MXE II supports the simulation of designs up to 1 million system gates and is sold as an option.

For more information, visit the Xilinx Design Tools Center at [www.xilinx.com/ise](http://www.xilinx.com/ise)

#### Corporate Headquarters

Xilinx, Inc.  
2100 Logic Drive  
San Jose, CA 95124  
Tel: (408) 559-7778  
Fax: (408) 559-7114  
Web: [www.xilinx.com](http://www.xilinx.com)

#### European Headquarters

Xilinx  
Citywest Business Campus  
Saggart,  
Co. Dublin  
Tel: +353-1-464-0311  
Fax: +353-1-464-0324  
Web: [www.xilinx.com](http://www.xilinx.com)

#### Japan

Xilinx, K.K.  
Shinjuku Square Tower 18F  
6-22-1 Nishi-Shinjuku  
Shinjuku-ku, Tokyo  
163-1118, Japan  
Tel: 81-3-5321-7711  
Fax: 81-3-5321-7765  
Web: [www.xilinx.co.jp](http://www.xilinx.co.jp)

#### Asia Pacific

Xilinx, Asia Pacific  
Unit 1201, Tower 6, Gateway  
9 Canton Road  
Tsimshatsui, Kowloon,  
Hong Kong  
Tel: 852-2-424-5200  
Fax: 852-2-494-7159  
E-mail: [ask-asiapac@xilinx.com](mailto:ask-asiapac@xilinx.com)



FORTUNE 2004  
100 BEST COMPANIES TO WORK FOR

© 2004 Xilinx Inc. All rights reserved. The Xilinx name is a registered trademark; Virtex-4, Spartan-3, Virtex-II Pro, XtremeDSP, and RocketIO are trademarks; and The Programmable Logic Company is a service mark of Xilinx Inc. All other trademarks are the property of their owners.